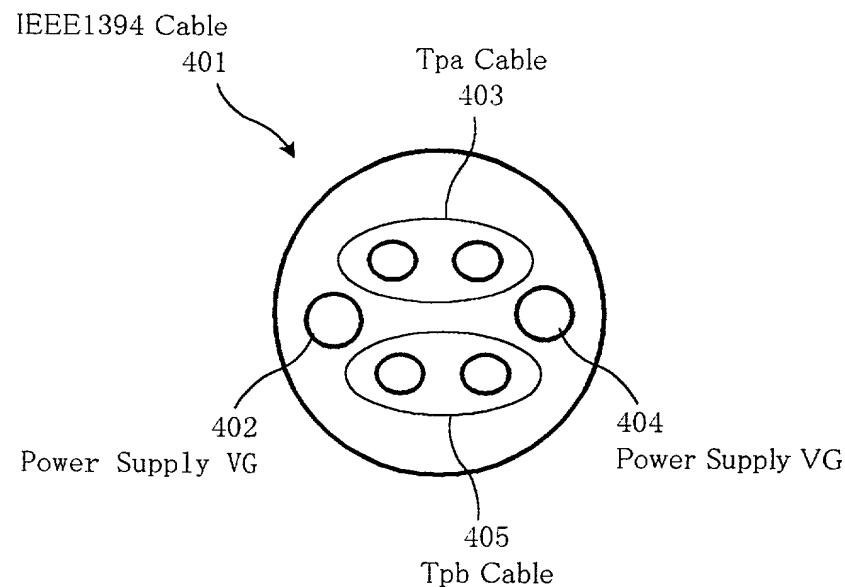


Fig. 1

Prior Art



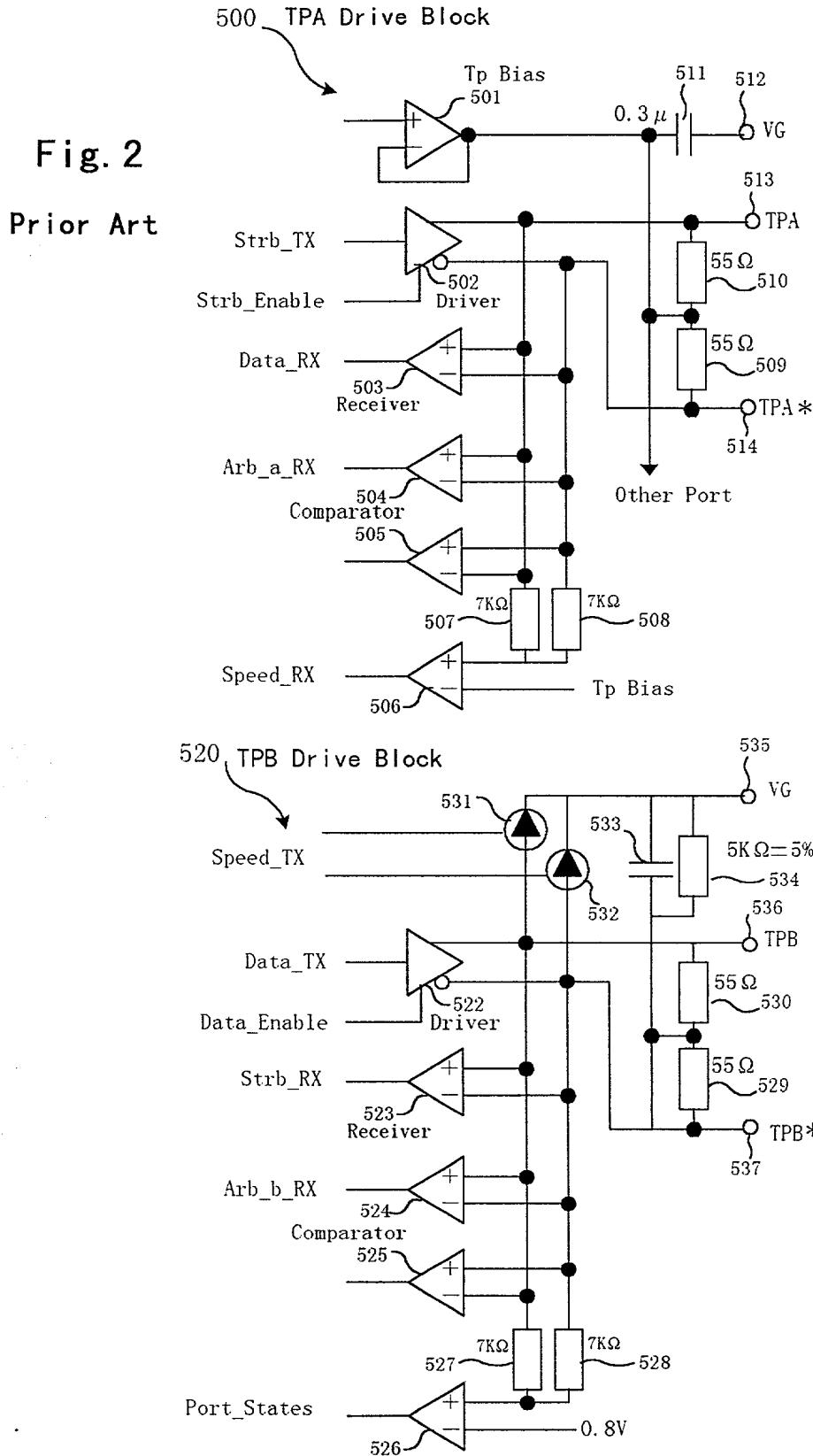


Fig. 3

Prior Art

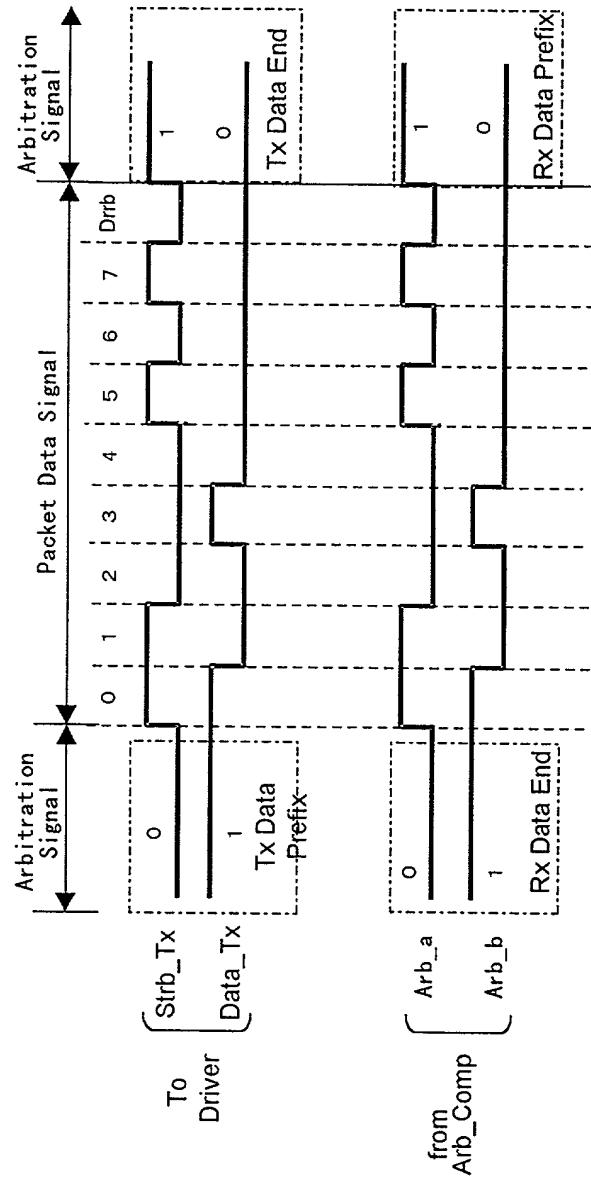


Fig. 4 Prior Art

arbitration transmit		Line state name
Arb_a_TX	Arb_b_TX	
Z	Z	IDLE
Z	0	TX_REQUEST
		TX_GRANT
0	Z	TX_PARENT_NOTIFY
0	1	TX_DATA_PREFIX
1	Z	TX_CHILD_NOTIFY
		TX_IDENT_DONE
1	0	TX_DATA_END
1	1	BUS_RESET

Fig. 5 Prior Art

arbitration Receive		Line state name
Arb_a_RX	Arb_b_RX	
Z	Z	IDLE
Z	0	RX_PARENT_NOTIFY
		RX_REQUEST_CANCEL
Z	1	RX_IDENT_DONE
0	Z	RX_SELF_ID_GRANT
		RX_REQUEST
0	0	RX_ROOT_CONTENTION
		RX_REQUEST
0	1	RX_PARENT_HANDSHAKE
		RX_DATA_END
1	Z	RX_CHILD_HANDSHAKE
1	0	RX_DATA_PREFIX
1	1	BUS_RESET

Fig. 6

Prior Art

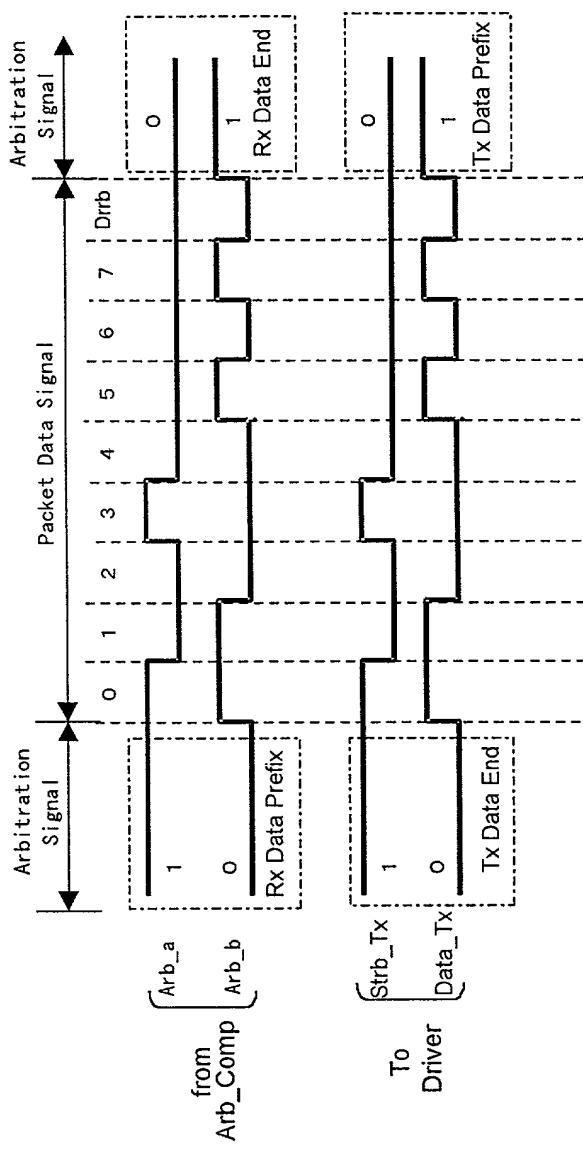


Fig. 7 Prior Art

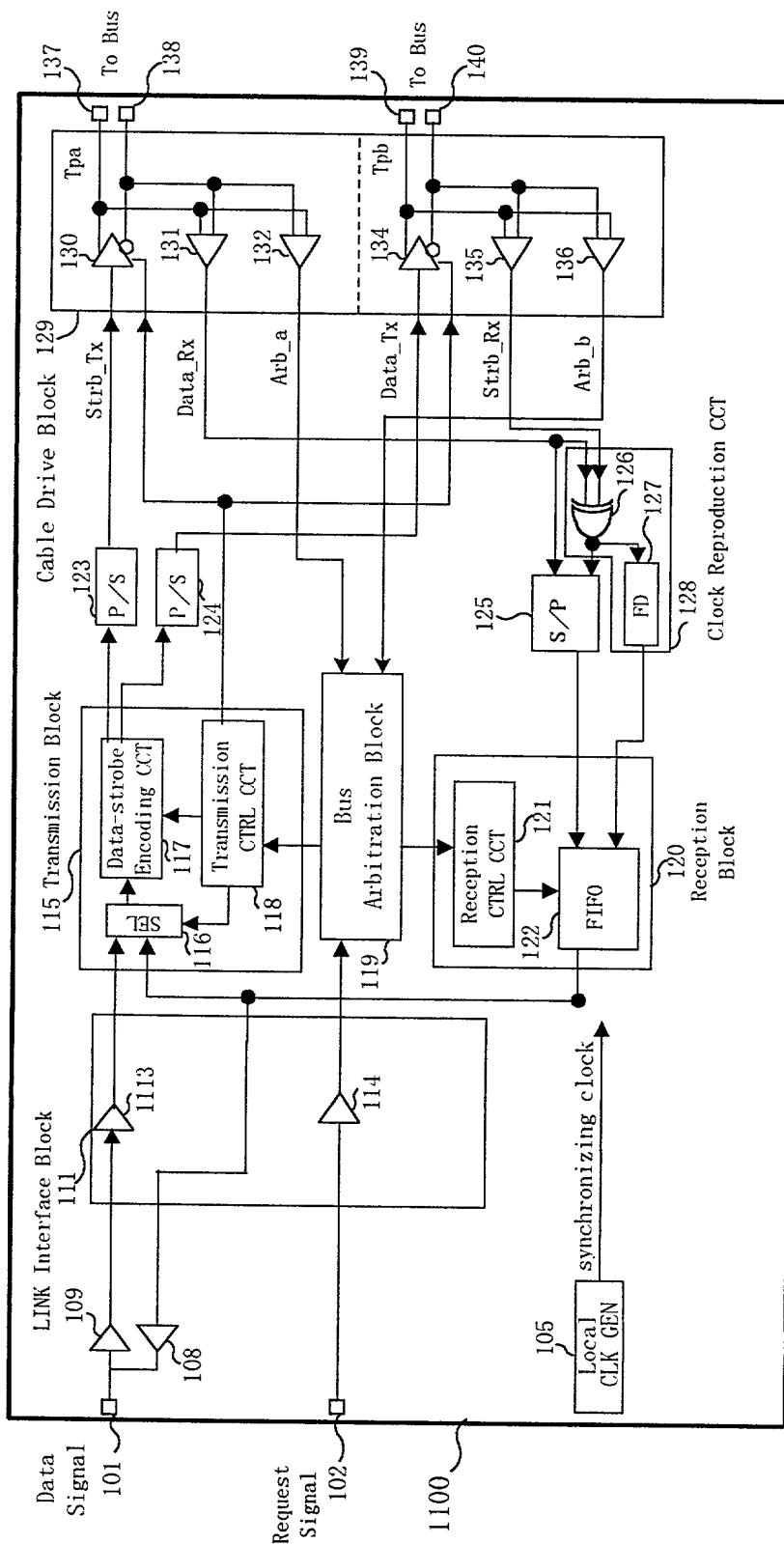


Fig. 8

Prior Art

arbitration transmit		State of FIFO
Arb_a_TX	Arb_b_TX	
Z	Z	Inactive
Z	0	Inactive
Z	1	Inactive
0	Z	Inactive
		Inactive
0	0	Inactive
		Inactive
0	1	Inactive
1	Z	Inactive
1	0	Active
1	1	Inactive

Fig. 9

Prior Art

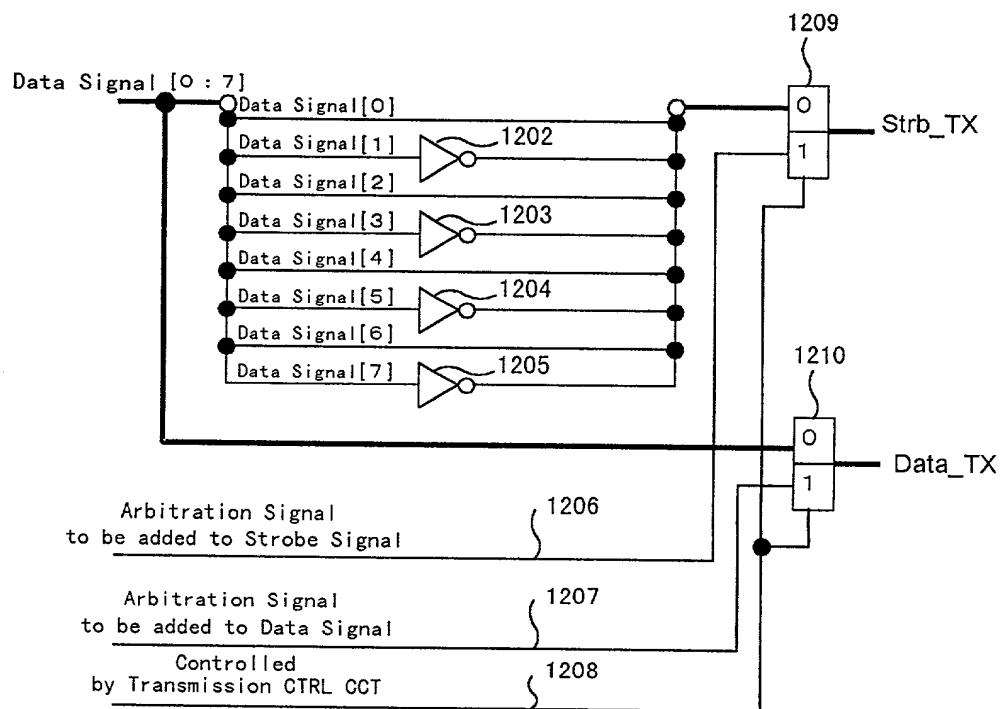


Fig. 10

Prior Art

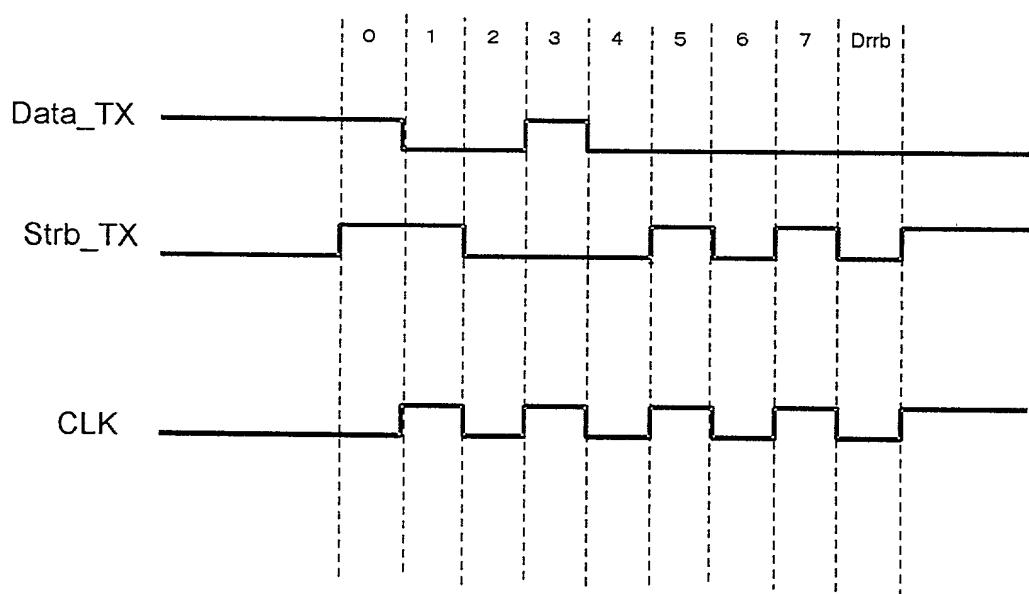


Fig. 11

Prior Art

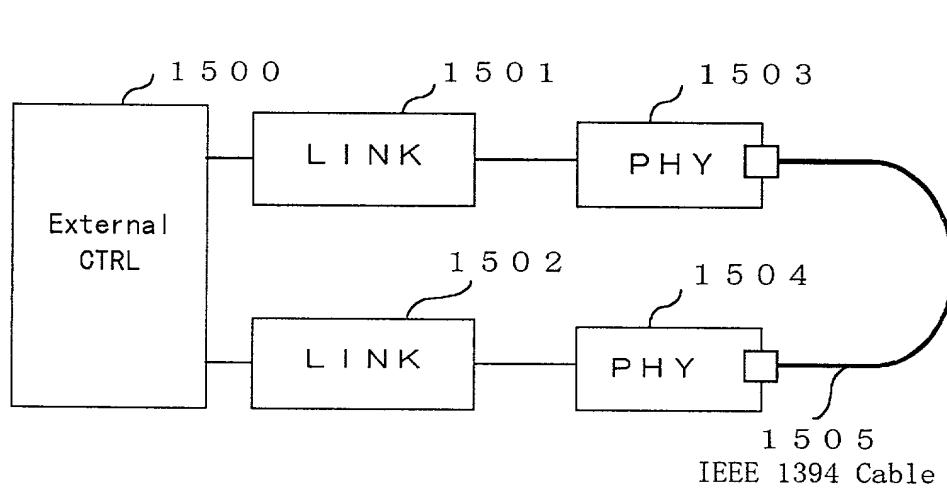


Fig. 12

Prior Art

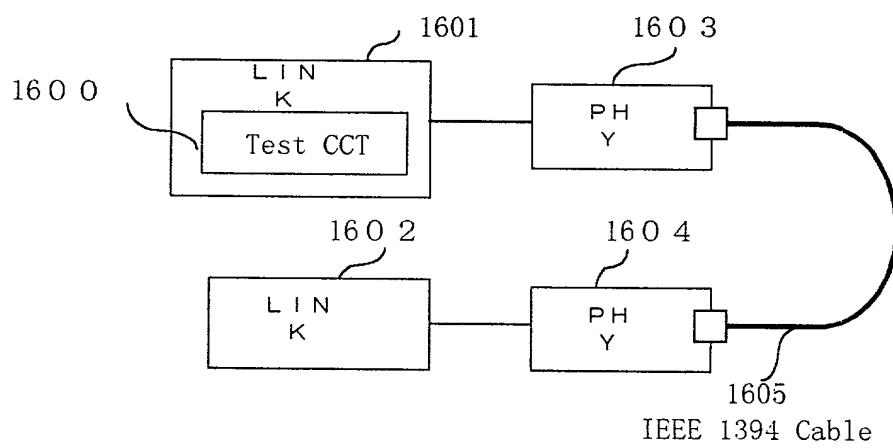


Fig. 13

Prior Art

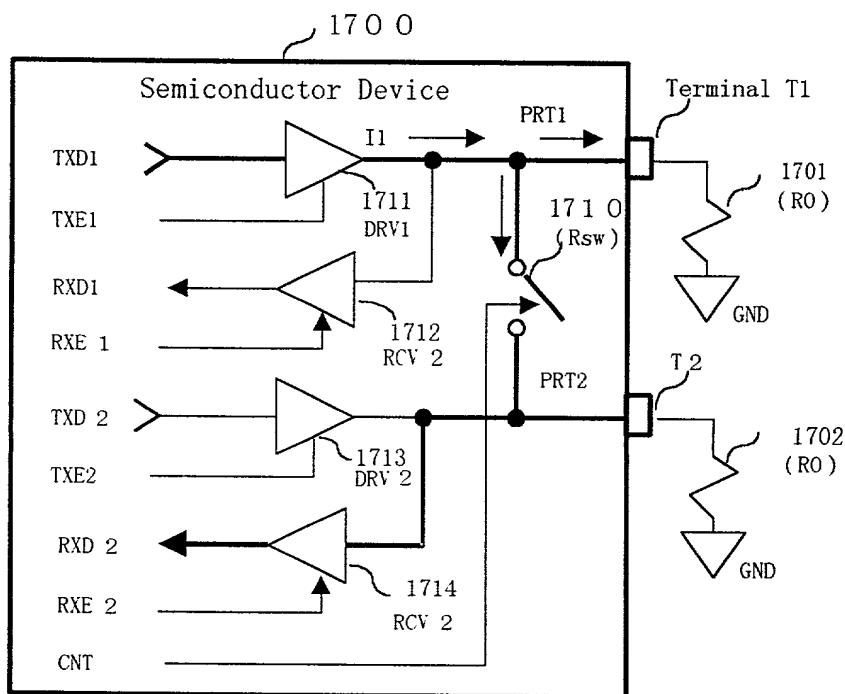


Fig. 14

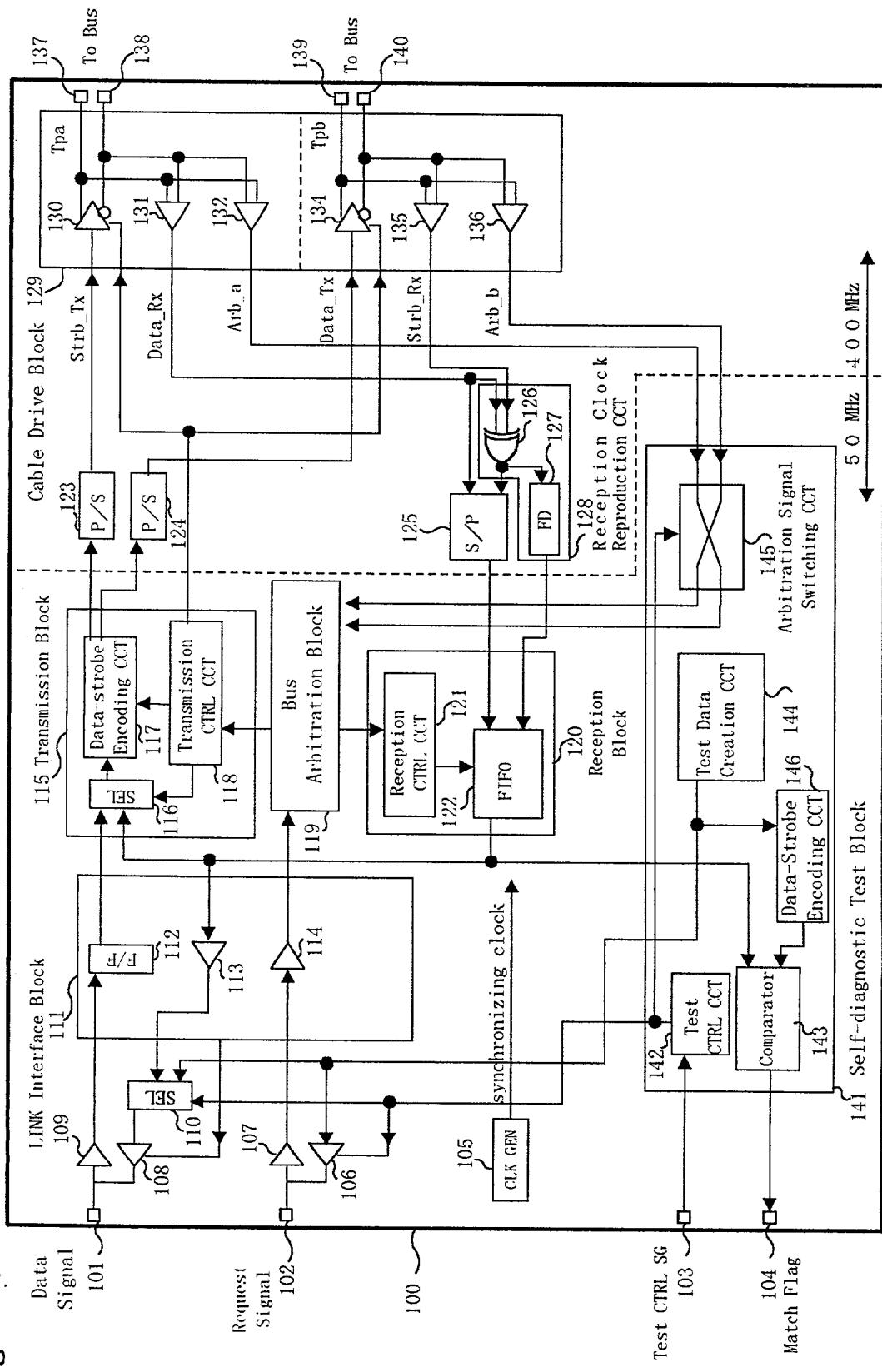


Fig. 15

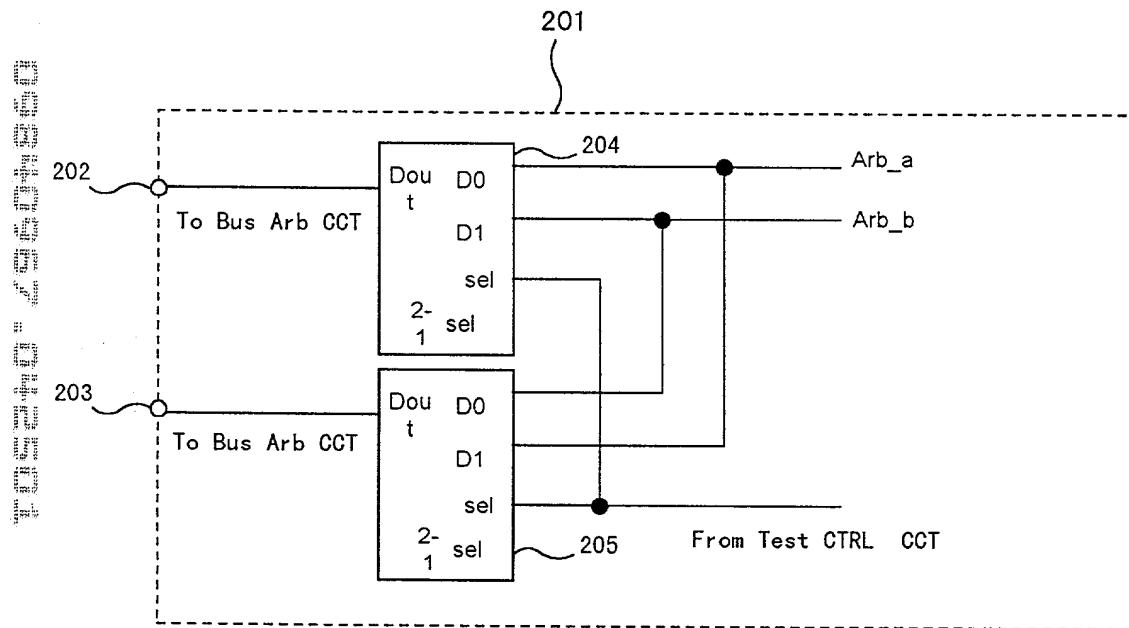


Fig. 16

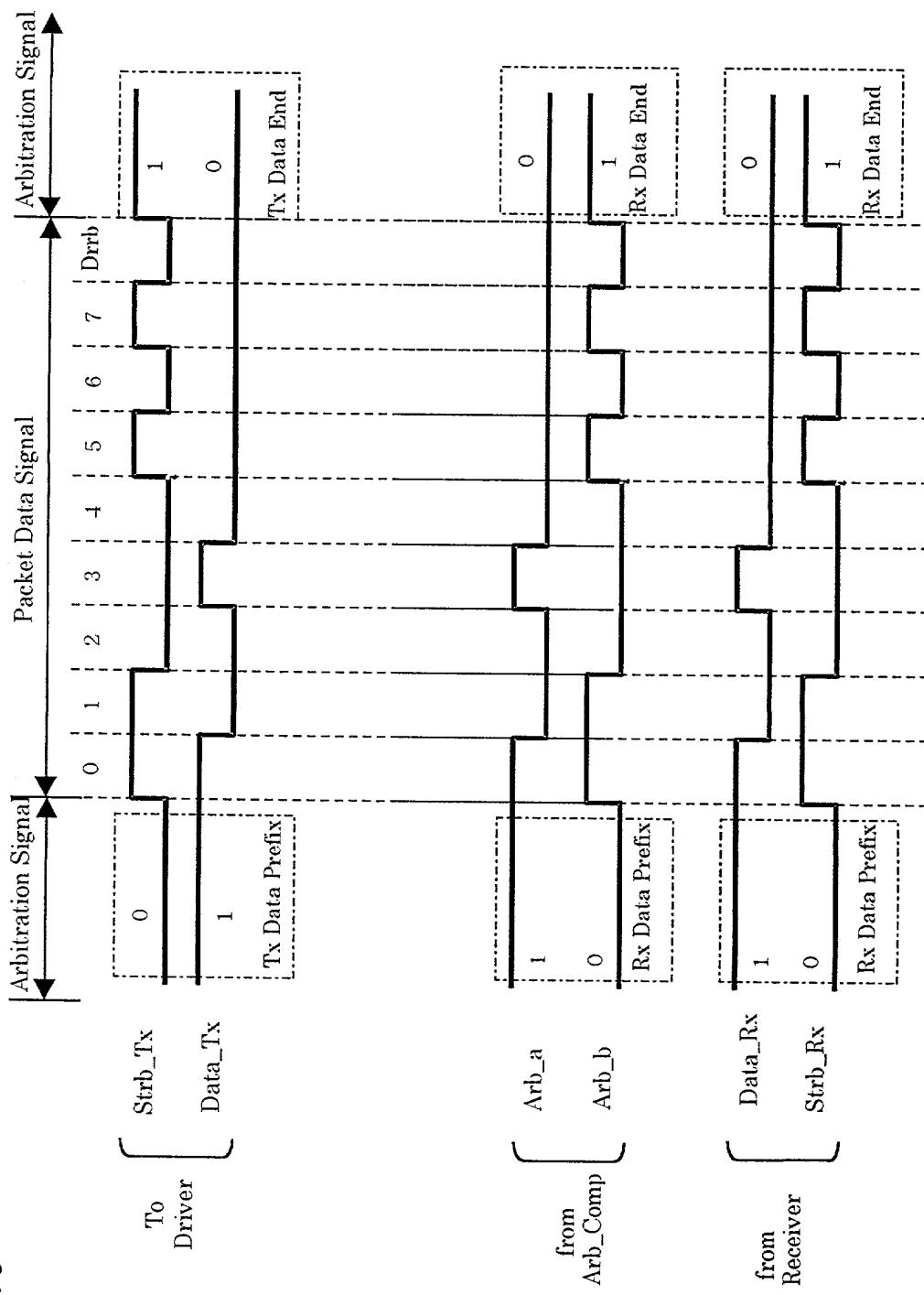


Fig. . 17

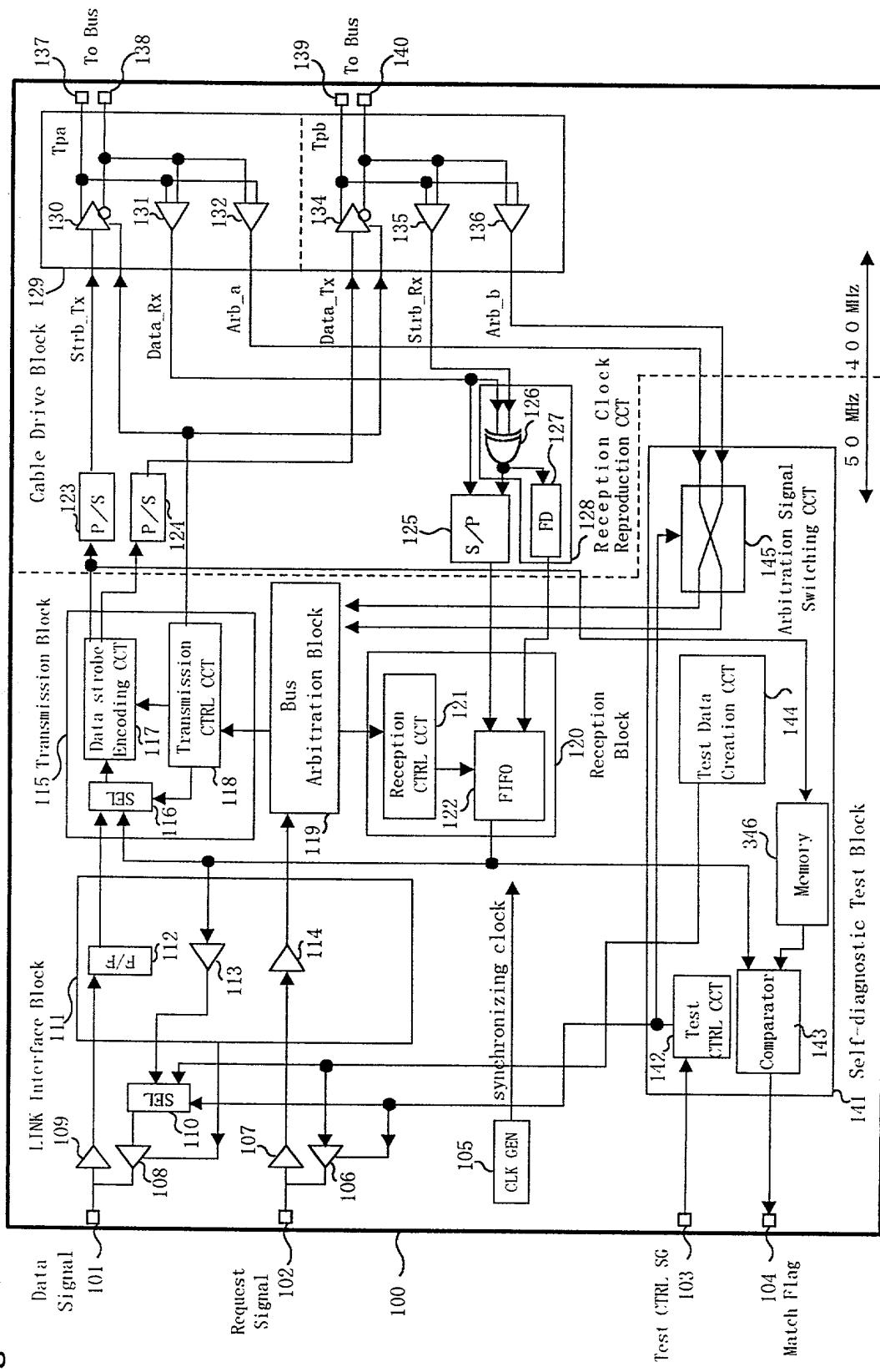


Fig. 18

